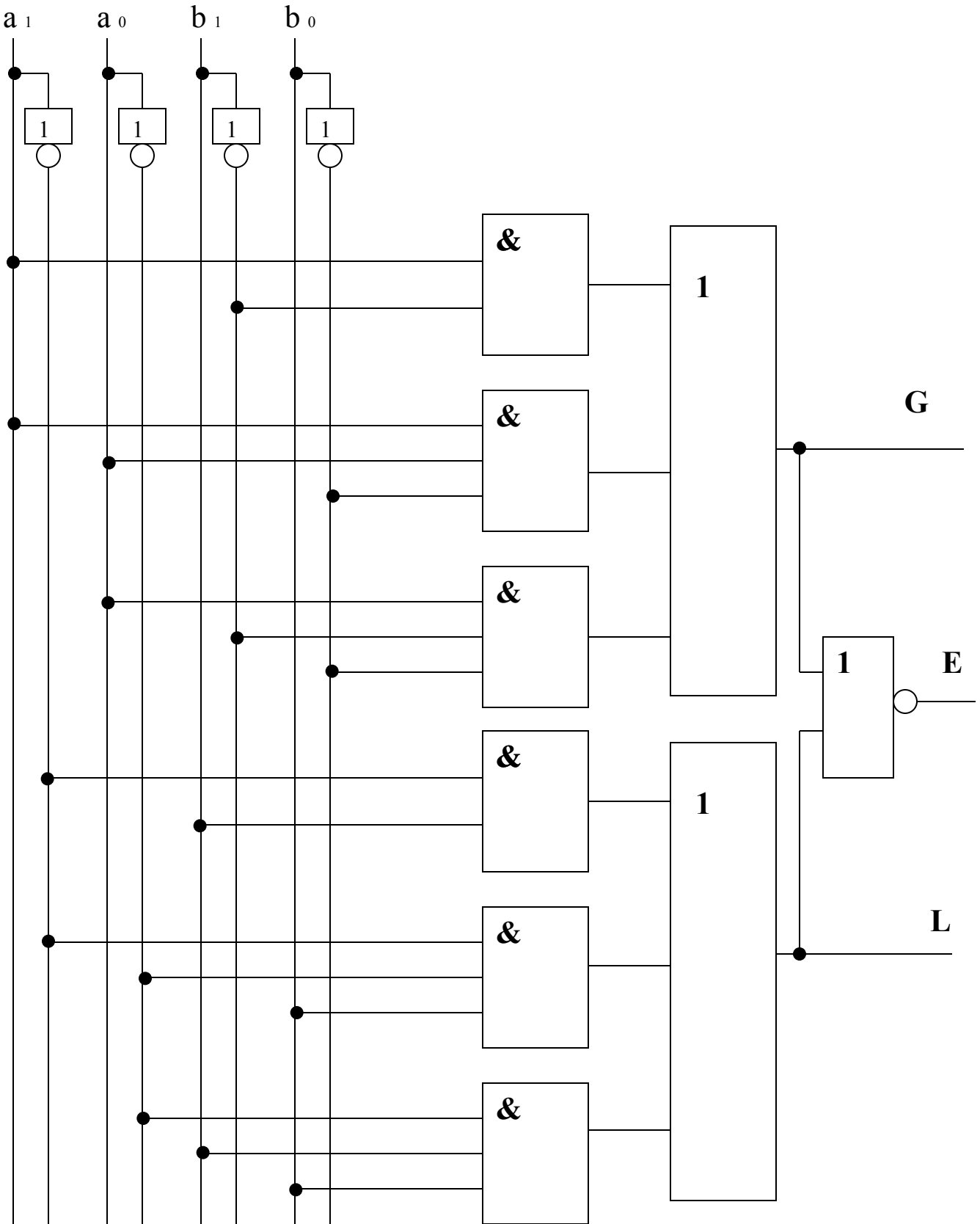


# Võrdlusskeem *Magnitude Comparator*

a <sub>0</sub>	=	G
a <sub>1</sub>		L
b <sub>0</sub>		
b <sub>1</sub>		E

a <sub>1</sub>	a <sub>0</sub>	b <sub>1</sub>	b <sub>0</sub>	G	L
0	0	0	0	0	0
0	0	0	1	0	1
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	1	0
0	1	0	1	0	0
0	1	1	0	0	1
0	1	1	1	0	1
1	0	0	0	1	0
1	0	0	1	1	0
1	0	1	0	0	0
1	0	1	1	0	1
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	0
1	1	1	1	0	0



10/08/202

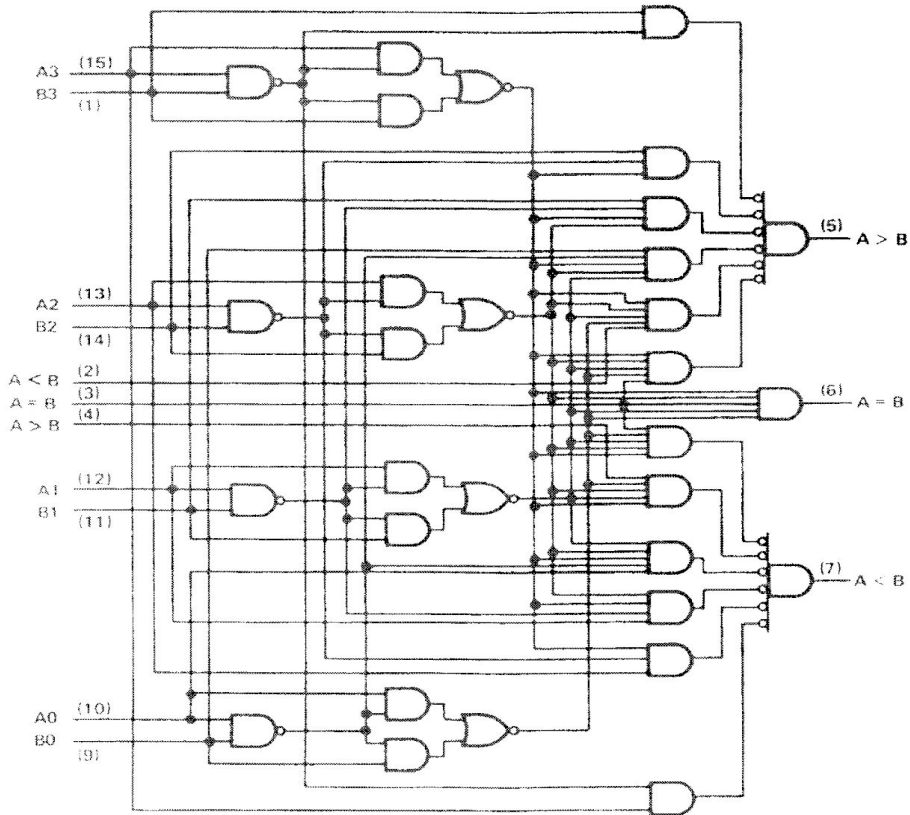
T. Evarson

2

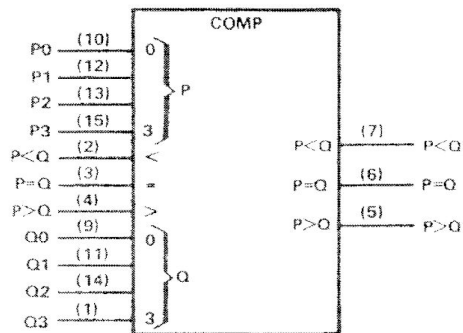
SN5485, SN54LS85, SN54S85  
 SN7485, SN74LS85, SN74S85  
**4-BIT MAGNITUDE COMPARATORS**

SDLS123 - MARCH 1974 - REVISED MARCH 1988

logic diagrams (positive logic)



logic symbol†



†This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, N, and W packages.



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